

# Static Program Analysis

## Part I of IV

# Automated Static Analysis

- A static analyzer is a software tool for source code text processing
- They parse the program text and try to discover potentially erroneous conditions and bring these to the attention of the V&V/Testing team
- Very effective as an aid to inspections.
- A supplement to but not a replacement for inspections

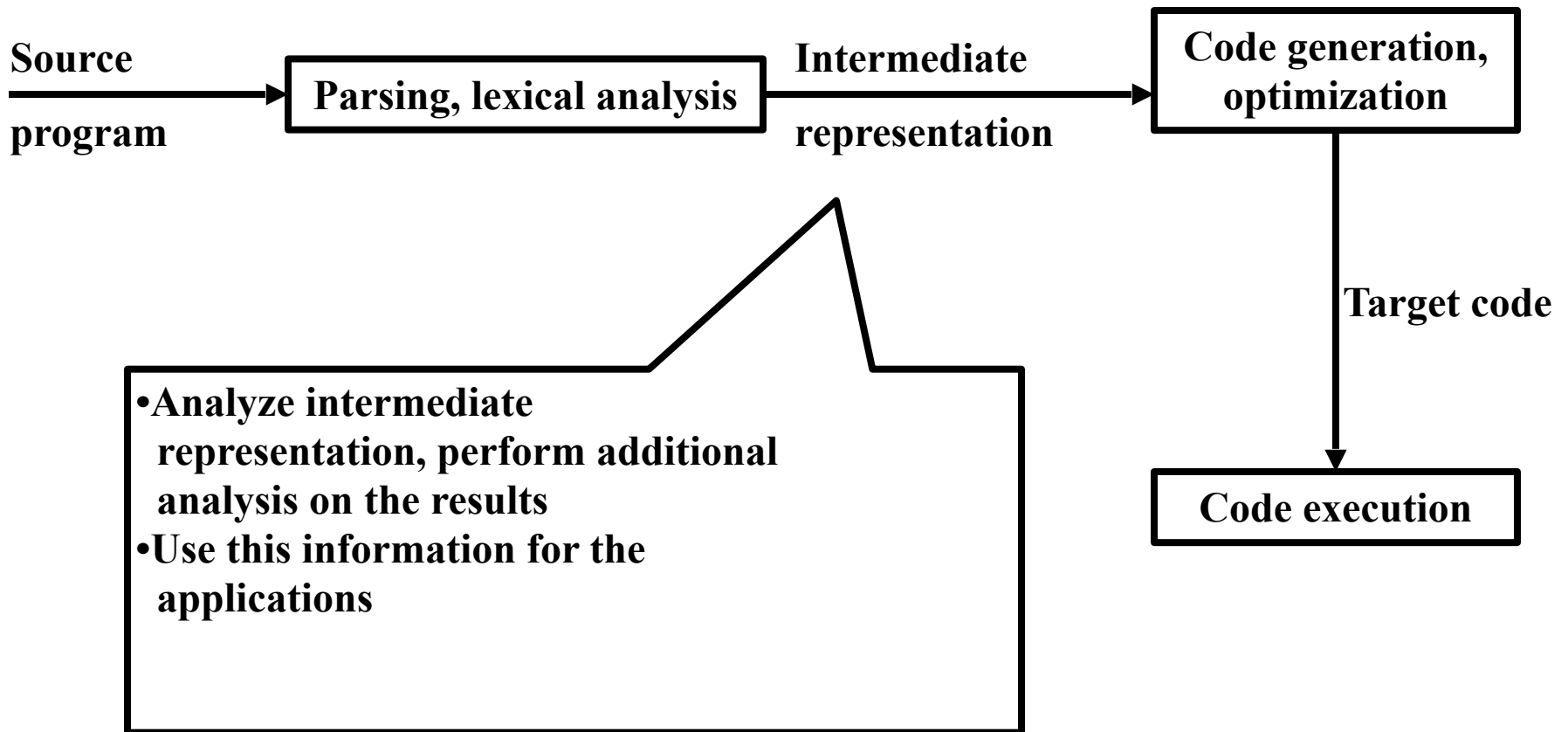
# Types of Static Analysis Checks

<b>Fault Type</b>	<b>Static Analysis Check</b>
Data	<ul style="list-style-type: none"><li>• Variables used before initialization</li><li>• Variables declared but never used</li><li>• Variables assigned twice but never used between assignments</li><li>• Possible array bound violations</li><li>• Undeclared variables</li></ul>
Control	<ul style="list-style-type: none"><li>• Unreachable code</li><li>• Unconditional branches into loops</li></ul>
I/O	<ul style="list-style-type: none"><li>• Variables output twice with no intervening assignment</li></ul>
Interface	<ul style="list-style-type: none"><li>• Parameter type mismatches</li><li>• Parameter number mismatches</li><li>• Non-usage of results of functions</li><li>• Uncalled functions</li></ul>
Storage management	<ul style="list-style-type: none"><li>• Unassigned pointers</li><li>• Pointer arithmetic</li></ul>

# Static Models of the Source Code

- Low level
  - Source code text
- Intermediate level
  - Symbol table
  - Parse tree
- High level
  - Control flow
  - Data flow
  - Program Dependency Graph
- Design Level
  - Class diagram
  - Sequence diagram

# Starting Point for Static Analysis



# Intermediate Representation

- Parse (derivation) Tree & Symbol Table
- Concrete Parse Tree
  - Concrete (derivation) tree shows structure *and* is language-specific issues
  - Parse tree represents concrete syntax
- Abstract Syntax Tree/Graph (AST)/(ASG)
  - Abstract Syntax Tree shows only structure
  - Represents abstract syntax

# AST vs Parse Tree

## Example

1.  $a := b + c$

- Grammar for 1
  - $\text{stmtlist} \rightarrow \text{stmt} \mid \text{stmt stmtlist}$
  - $\text{stmt} \rightarrow \text{assign} \mid \text{if-then} \mid \dots$
  - $\text{assign} \rightarrow \text{ident} \text{"="} \text{ident binop ident}$
  - $\text{binop} \rightarrow \text{"+"} \mid \text{"-"} \mid \dots$

2.  $a = b + c;$

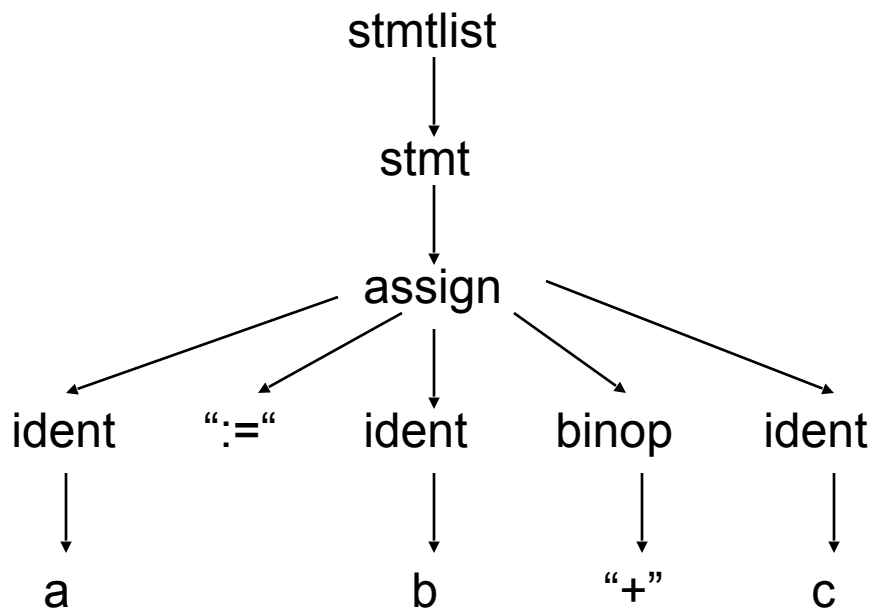
- Grammar for 2
  - $\text{stmtlist} \rightarrow \text{stmt} \text{";"} \mid \text{stmt} \text{";"} \text{stmtlist}$
  - $\text{stmt} \rightarrow \text{assign} \mid \text{if-then} \mid \dots$
  - $\text{assign} \rightarrow \text{ident} \text{"="} \text{ident binop ident}$
  - $\text{binop} \rightarrow \text{"+"} \mid \text{"-"} \mid \dots$

# Parse Trees

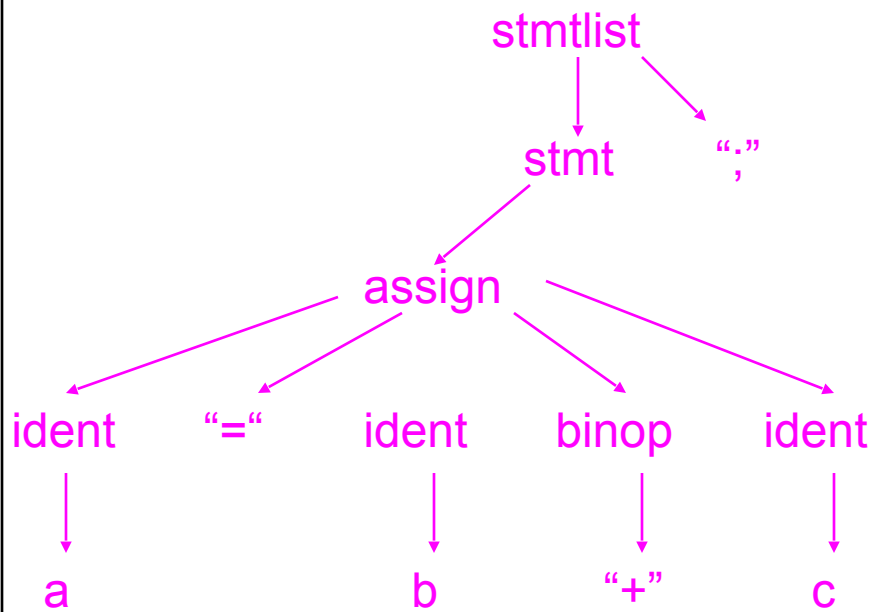
## Example

1. `a := b + c`
2. `a = b + c;`

Parse Tree for 1



Parse Tree for 2



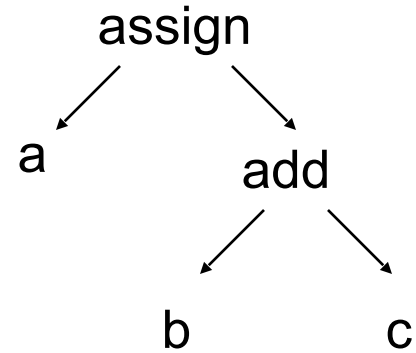


# AST

## Example

1. `a := b + c`
2. `a = b + c;`

Abstract syntax tree for 1 and 2



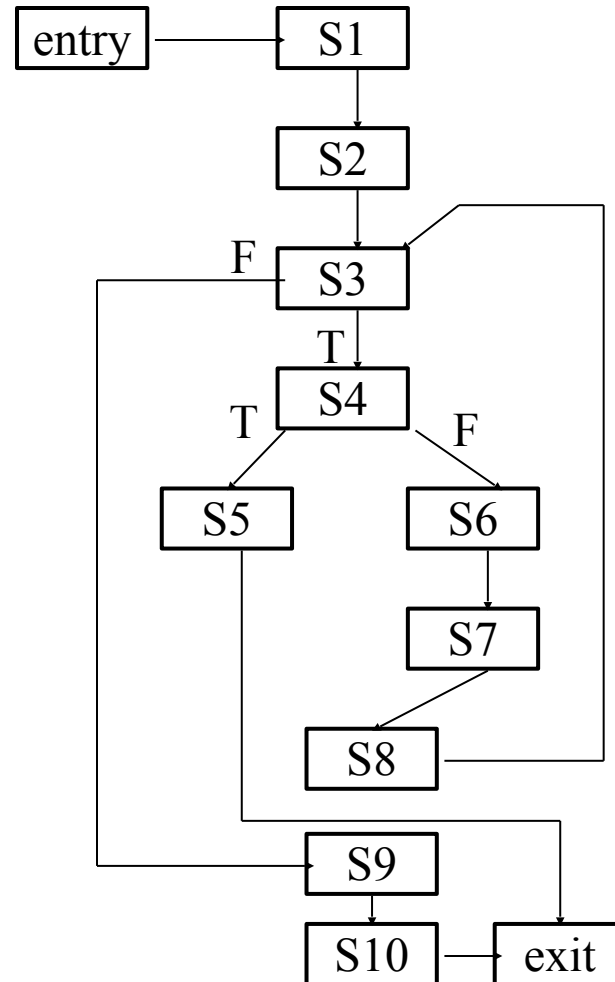
# Intermediate to High level

- Given
  - Source code
  - AST
  - Symbol table
- One can construct
  - Call graphs
  - Control flow graph
  - Data flow
  - Slices

# Control Flow Analysis (CF)

## Procedure AVG

```
S1  count = 0
S2  fread(fp_ptr, n)
S3  while (not EOF) do
S4    if (n < 0)
S5      return (error)
    else
S6      nums[count] = n
S7      count ++
    endif
S8    fread(fp_ptr, n)
    endwhile
S9  avg = mean(nums, count)
S10 return (avg)
```



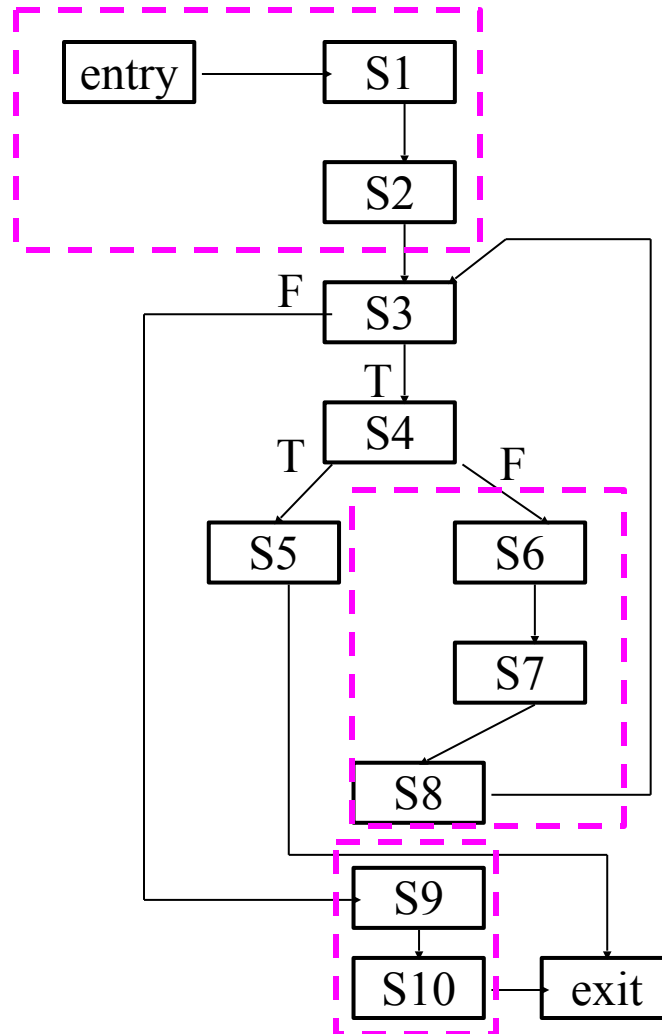
# Computing Control Flow

- Basic blocks can be identified in the AST
- Basic blocks are straight line sequence of statements with no branches in or out.
- A basic block may or may not be “maximal”
- For compiler optimizations, maximal basic blocks are desirable
- For software engineering tasks, basic blocks that represent one source code statement are often used

# Computing Control Flow

## Procedure AVG

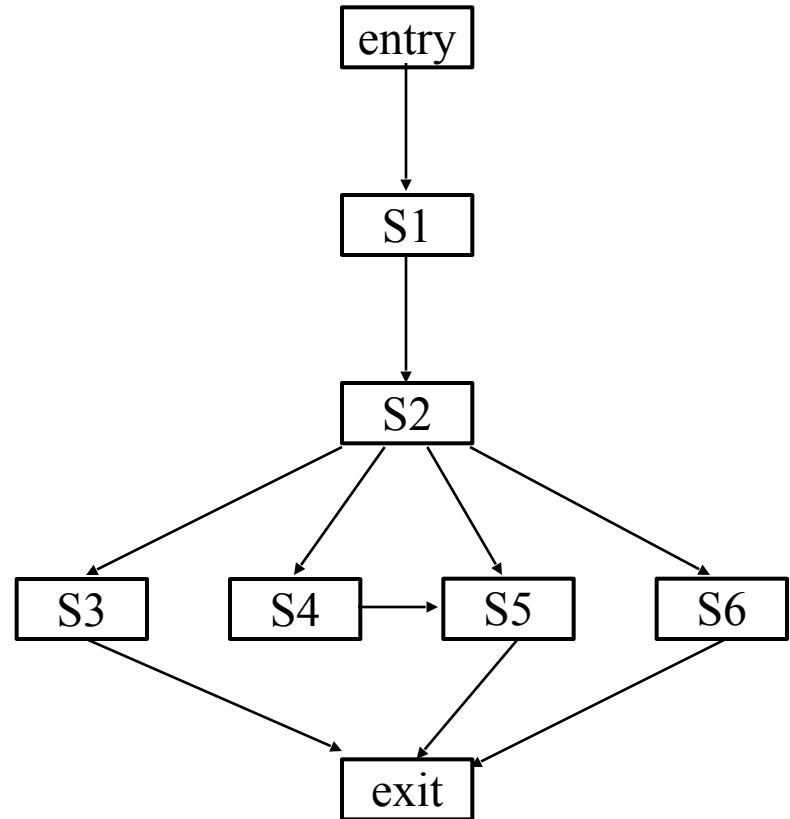
```
S1  count = 0
S2  fread(fpstr, n)
S3  while (not EOF) do
S4    if (n < 0)
S5      return (error)
    else
S6      nums[count] = n
S7      count ++
    endif
S8    fread(fpstr, n)
  endwhile
S9  avg = mean(nums, count)
S10 return (avg)
```



# Computing Control Flow

Procedure Trivial

```
S1  read (n)
S2  switch (n)
      case 1:
S3    write ("one")
      break
      case 2:
S4    write ("two")
      case 3:
S5    write ("three")
      break
      default
S6    write ("Other")
      endswitch
end Trivial
```



# Computing Control Flow

Procedure Trivial

```
S1  read (n)
S2  switch (n)
      case 1:
S3    write ("one")
      break
      case 2:
S4    write ("two")
      case 3:
S5    write ("three")
      break
      default
S6    write ("Other")
      endswitch
end Trivial
```

