Class Schedule — Computer Org. & Assembly Lang. — (1 December 1998)

Date	Lec	Hw Due (Lecs)	Lecture Topic (Section in Text)
8/31	01		Course Introduction, History of Computing
9/7			**Class Cancelled (Labor Day)**
9/14	06	HW1 (01–05)	Combinational Circuits: Karnaugh Maps (2.1)
9/21			**Exam 1 (Lectures 01–07)**
9/28	11	HW2 (06–10)	Memory Hierarchy (3.1)
10/5	13		CPU and I/O (3.2, 3.3)
10/12	16	HW3 (11–16)	Operands (4.3, 4.4), Flow of Control (4.5)
10/19	18		Assembler Directives (5.1)
10/26	21	HW4 (17–20)	Subroutines (Chapter 6)
11/2	24		SPARC Architecture and Debugger (4.7, 5.6)
11/9	V1	HW5 (21–26)	VAX Architecture
11/16	27		Interrupts (12.2, 12.3)
11/23	30		Microprogramming (9.5)
11/30	31	HW6 (V1–29)	Instruction Speedup (9.2)
12/7	34	HW7 (30–33)	Operating Systems

Date	Lec	Hw Due (Lecs)	Lecture Topic (Section in Text)
9/2	02		History of Computing
9/9	04		Encoding (1.2–1.3), Errors (1.4)
9/16	07		Combinational Circuits: Adders (2.1)
9/23	09		Components (2.3)
9/30			**Class Cancelled**
10/7	14		Disk Systems
10/14	17		Flow of Control (4.5), Bit Manipulation (4.6)
10/21	19		Program Translation (10.1–10.3)
10/28	22		Integer Representation (Chapter 7)
11/4	25		SPARC Architecture and Assembler (4.7, 5.6)
11/11			**Class Cancelled (Veterans Day)**
11/18	28		Instruction Implementation (9.1)
11/25			**Class Cancelled (early Thanksgiving)**
12/2	32		Pipelining (9.3, 9.4)
12/9			**Exam 4 (Lectures 27–34)**

Date	Lec	Hw Due (Lecs)	Lecture Topic (Section in Text)
9/4	03		Positional Number Systems (1.1)
9/11	05		Combinational Circuits: Boolean Algebra (2.1)
9/18	08		Sequential Circuits: Storage (2.2)
9/25	10		Integrated Circuits (2.1.6, 2.3)
10/2	12		Memory Systems (3.1)
10/9	15		Instruction Addresses (4.1), Registers (4.2)
10/16			**Exam 2 (Lectures 08–16)**
10/23	20		Addressing Modes (5.2)
10/30	23		Floating-Point Numbers (8.1–8.2)
11/6	26		SPARC Architecture (4.7, 5.6)
11/13			**Exam 3 (Lectures 17–26)**
11/20	29		Instruction Implementation (9.1)
11/27			**Class Cancelled (Thanksgiving)**
12/4	33		Parallel Systems (13)
12/11	35		Discussion of Final Exam

Final Exam will be on Wednesday 16 December, from 10:15am–12:30pm, in the usual classroom (MSB 115). Closed book and closed notes, but calculators are allowed.