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CS 4/55111

## Take-Home Final Exam

VLSI Design

Due to me by 5pm on Tuesday 9 May 2000

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Write 5-7 pages of typed text (single-spaced, either ASCII text or 12-point word-processor output) summarizing some aspect of VLSI design not discussed in class. Examples might include some aspect of CMOS fabrication, algorithms for placing and routing components on a chip, a description of FPLDs from some other vendor, some aspect of behavioral synthesis, etc. If you're not sure that your topic is appropriate, send me an email with a brief description and I'll let you know if it's acceptable or not. Include a bibliography of any references cited in your report.

If you include figures or algorithms, they must be in addition to the page limit specified above. Part of the goal here is to write as clear a description as possible in a limited amount of space—a very “real world” experience. Also, be certain this description is in entirely your own words.

I will leave some books in the VLSI Design Lab that you might find useful. In addition, you might want to look at the proceedings of the Design Automation Conference for ideas — online at <http://www.sigda.org/Archives/ProceedingArchives>.

A printout of your report is due to me by Tuesday 9 May at 5pm, along with your key to the VLSI Design Lab (if you have one). I will be out of town from Tuesday morning 2 May though Monday night 8 May, but should be reachable by email while away.