Class Schedule

Principles of Customized VLSI Design

(10 May 2000)

Date	Lec	Due (Lecs)	Lecture Topic (Section in Text)	Date	Lec	Due (Lecs)	Lecture Topic (Section in Text)	Date	Lec	Due (Lecs)	Lecture Topic (Section in Text)
				1/19	01		Course Introduction, ASIC Design Flow (Smith 1.2)	1/21	02		Types of ASICs (Smith 1.1)
1/24	03		Economics of ASICs (Smith 1.4)	1/26	04		Combinational Logic	1/28	05		Combinational Logic
1/31	06		Sequential Logic	2/2	07		State Machines	2/4	08		Building Blocks of a Computer
2/7	09		Schematic Capture (Smith 9.1, Salcic 3.1, Hamble 1.1)	2/9	10		Altera MAX+PLUS II (Salcic 3.4, Hamblen 1.2–1.4)	2/11	11		Altera MAX+PLUS II (Salcic 3.4, Hamblen 1.5–1.13)
2/14	12		Using Altera MAX+PLUS II (Hamblen 2, 4, 5)	2/16	13		CMOS Transistors & Gates (Smith 2.1)	2/18	14		CMOS Fabrication & Design Rules (Smith 2.2–2.3)
2/21	15		Toward FPLDs	2/23	16		Types of FPLDs (Salcic 1)	2/25	17	P1	Actel ACT
2/28	18		Altera MAX (Salcic 2.1)	3/1	19		Altera FLEX 8000 (Salcic 2.2)	3/3	20	P2 (prop)	Altera FLEX 10K (Salcic 2.2)
3/6	21		Xilinx XC4000 (Salcic 2.3)	3/8			**Class Cancelled (Walker out of town)**	3/10			**Class Cancelled (Walker out of town)**
3/13			**Exam 1 (Lecture 01–21)**	3/15			**Class Cancelled (Long Class on 3/17)**	3/17		P2	**Project Presentations (in VLSI Design Lab)**
Kent Spring Break											
3/27	22		AHDL (Salcic 4)	3/29	23		AHDL (Salcic 4)	3/31			**Class Cancelled (Walker out of town)**
4/3	24		AHDL (Salcic 4)	4/5	25		AHDL Examples	4/7			**Class Cancelled (Walker out of town)**
4/10	26	P3	AHDL Design Examples (Salcic 5)	4/12	27		SimP Microprocessor (Salcid 6)	4/14			**Class Cancelled (Walker out of town)**
4/17	28		VHDL Overview (Hamblen 6)	4/19	29	P3	VHDL Overview (Hamblen 6)	4/21	30		VHDL Examples
4/24	31		VHDL Examples (Hamblen 13), VHDL (Salcic 7)	4/26	32		VHDL (Salcic 7)	4/28	33		Behavioral Synthesis
5/1		P4	**Project Presentations (in VLSI Design Lab)**	5/3			**Class Cancelled (Long Class on 5/1)**	5/5			**Class Cancelled (Long Class on 5/1)**
5/9	**5pm**		**Take-Home Final Exam Due**								