Altera's MAX+PLUS II Development System

■ Supports Altera MAX and FLEX devices

■ Design entry includes: schematic capture, waveform entry, and the AHDL, VHDL, and Verilog hardware description languages (HDLs) [Verilog ≥ 9.xx only]

■ Documentation:

  ● Tutorials in Hamblen Chapters 1 and 4

  ● Brief overview in Salcic Section 3.4

  ● Altera's "data sheet" overview (~20 pages) (link on class web page)

  ● Altera's "Getting Started" manual (~ 350 pages) (link on class web page)
    ■ Detailed overview in Chapter 2, "A Perspective" (~80 pages)
      – Printout(s) in VLSI Design Lab
    ■ Chapter 3, "Tutorial"

Using the MAX+PLUS II Software

■ 9.23 Student Edition in Salcic book
  10.1 Student Edition in Hamblen book
  10.2 Baseline Edition on Altera web site

  ● Differences from commercial version:
    ■ Can't use non-Altera design entry tools
    ■ Only supports devices on UP1 Educ. Board

■ Install on your home PC if desired

  ● Install the software
    ■ Hard to location on Altera web site
    ■ Products / Design Software / Legacy Software / Max+Plus II BASELINE Software
    ■ (Ignore text trying to get you to download the newer Quartus product)

  ● Use form on web page to send your disk drive serial number to Altera, wait for license file from Altera by email
    ■ *Store license file with MAX+PLUS II*
    ■ *Install into MAX+PLUS II to enable tools (Options / License Setup, select license file)*

Using the MAX+PLUS II Software

■ Procedure for projects:

  ● Work on projects at home, or in the VLSI Design Lab (MSB 353)

  ● Then come to the VLSI lab to download and test on a UP1 Education Board

■ Buy a UP-2 Education Board if you like

  ● Education & Events / University Program / Educational Materials / Development & Education Boards / UP2

  ● $99 from Altera eStore